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0123-03 2183

DOCKET NO.: A00312.70410.US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Technology Center 2100

Applicant: Plante et al.
Serial No: 09/923,225
Confirmation. No.: 8975
Filed: August 6, 2001
For: HIGH PERFORMANCE TURBO AND VITERBI CHANNEL
DECODING IN DIGITAL SIGNAL PROCESSORS

Examiner: Not yet assigned
Art Unit: 2183

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, addressed to Commissioner for Patents, Washington, D.C. 20231, on the 13 day of January, 2003.

William R. McClellan
William R. McClellan, Reg. No. : 29,409

Commissioner For Patents
Washington, D.C. 20231

Sir:

Transmitted herewith are the following documents:

- ☒ Information Disclosure Statement
- ☒ PTO Form 1449 with cited references
- ☒ Return Receipt Postcard

If the enclosed papers are considered incomplete, the Mail Room and/or the Application Branch is respectfully requested to contact the undersigned at (617) 720-3500, Boston, Massachusetts.

A check is not enclosed. If a fee is required, the Commissioner is hereby authorized to charge Deposit Account No. 23/2825. A duplicate of this sheet is enclosed.

Respectfully submitted,
Plante et al., Applicant

By: *William R. McClellan*
William R. McClellan, Reg. No.: 29,409
Wolf, Greenfield & Sacks, P.C.
600 Atlantic Avenue
Boston, Massachusetts 02210-2211
Telephone: (617)720-3500

Docket No. A00312.70410.US
Date: January 13, 2003
xNDDx





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William R. McClellan
William R. McClellan, Reg. No. 29,409

Commissioner for Patents
Washington, D.C. 20231

**STATEMENT FILED PURSUANT TO THE DUTY OF
DISCLOSURE UNDER 37 CFR §§1.56, 1.97 AND 1.98**

Sir:

Pursuant to the duty of disclosure under 37 C.F.R. §§1.56, 1.97 and 1.98, the Applicant requests consideration of this Information Disclosure Statement.

PART I: Compliance with 37 C.F.R. §1.97

This Information Disclosure Statement has been filed before the mailing date of a first Office Action on the merits in the above-identified case. No fee or certification is required.

PART II: Compliance with 37 CFR §1.704(d)

Each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart application and this communication was not received by the office of the undersigned more than 30 days prior to filing of this Information Disclosure Statement.

PART III: Information Cited

The Applicant hereby makes of record in the above-identified application the information listed on the attached form PTO-1449 (modified). The order of presentation of the references should not be construed as an indication of the importance of the references.

The Applicant would like to bring to the Examiner's attention the enclosed search report from the corresponding International Application No. PCT/US02/24703 having an International filing date of August 6, 2002.

PART IV: Remarks

Documents cited anywhere in the Information Disclosure Statement are enclosed unless otherwise indicated. It is respectfully requested that:

1. The Examiner consider completely the cited information, along with any other information, in reaching a determination concerning the patentability of the present claims;
2. The enclosed form PTO-1449 be signed by the Examiner to evidence that the cited information has been fully considered by the Patent and Trademark Office during the examination of this application;
3. The citations for the information be printed on any patent which issues from this application.

By submitting this Information Disclosure Statement, the Applicant makes no representation that a search has been performed, of the extent of any search performed, or that more relevant information does not exist.

By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, in fact, prior art as defined by 35 U.S.C. §102.

Notwithstanding any statements by the Applicant, the Examiner is urged to form his own conclusion regarding the relevance of the cited information.

Serial No.: 09/923,225

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Art Unit: 2183

An early and favorable action is hereby requested.

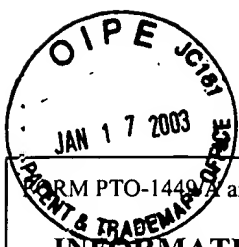
Respectfully submitted,
Plante et al., Applicant

By: William R. McClellan
William R. McClellan, Reg. No. 29,409
Wolf, Greenfield & Sacks, P.C.
600 Atlantic Avenue
Boston, Massachusetts 02210-2211
Telephone: (617) 720-3500

Docket No. A00312.70410.US

Date: January 13, 2003

XNDDX



INFORMATION DISCLOSURE STATEMENT BY APPLICANT		APPLICATION NO.: 09/923,225		ATTY. DOCKET NO.: A00312.70410.US	
		FILING DATE: August 6, 2001		CONFIRMATION NO.: 8975	
		APPLICANT: Plante et al.			
		GROUP ART UNIT: 2183		EXAMINER: Not yet assigned	
Sheet	1	of	1		

U.S. PATENT DOCUMENTS

Examiner's Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YYYY
		Number	Kind Code		
	1	5,511,081		Hagenauer	04-23-1996
	2	5,933,462		Viterbi et al.	08-03-1999
	3	6,028,899		Petersen	02-22-2000
	4	6,192,084	B1	Miyauchi et al.	02-20-2001
	5	6,343,368		Lerzer	01-29-2002
	6	6,393,076	B1	Dinc et al.	05-21-2002

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FOREIGN PATENT DOCUMENTS

Examiner's Initials	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document (not necessary)	Date of Publication of Cited Document MM-DD-YYYY	Translation (Y/N)
		Office/ Country	Number	Kind Code			

OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Initials	Cite No	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)	
	7	BENEDETTO ET AL., "Soft-Output Decoding Algorithms in Iterative Decoding of Turbo Codes", TDA Progress Report 42-124, February 15, 1996, pp. 63-87		
	8	BENEDETTO ET AL., "A Soft-Input Soft-Output Maximum A Posteriori (MAP) Module to Decode Parallel and Serial Concatenated Codes", TDA Progress Report 42-127, November 15, 1996, pp. 1-20		
	9	DIVSALAR ET AL., "Hybrid Concatenated Codes and Iterative Decoding", TDA Progress Report 42-130, August 15, 1997, pp. 1-23		
	10	ANDREW J. VITERBI, "An Intuitive Justification and a Simplified Implementation of the MAP Decoder for Convolutional Codes", IEEE Journal on Selected Areas in Communications, Vol. 16, No. 2, February 1998, pp. 260-264		
	11	YE LIU ET AL., "MAP Algorithms for Decoding Linear Block Codes Based on Sectionalized Trellis Diagrams", IEEE, 1998, pp. 562-566		
	12	YE LIU ET AL., "MAP Algorithms for Decoding Linear Block Codes Based on Sectionalized Trellis Diagrams", IEEE Transactions on Communications, Vol. 48, No. 4, April 2000, pp. 577-587		

EXAMINER	DATE CONSIDERED
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#EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

*a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. __, filed __, and relied upon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).

[NOTE - Must provide a copy of any patent, publication, other information listed, even if it was previously submitted to, or cited by, the U.S. Patent Office in an earlier application, unless the earlier application is identified by the IDS and is relied upon for an earlier filing date under 35 U.S.C. §120, and the copy was provided in the earlier application.]